

## **REDUCING IMAGE SENSOR LAG**

### **ABSTRACT**

Systems and methods of reducing image sensor lag are described. In one aspect, an image sensor includes multiple pixels, pixel circuits, and a bias circuit.

5 Each of the pixels includes a respective photodiode region. Each of the pixel circuits is operable to control integration and readout steps for a respective pixel. The bias circuit is operable to apply voltages across the pixels to induce carrier injection into the photodiode regions to reduce image lag. In another aspect, non-reverse-bias flow of carriers is induced in the photodiode regions of image sensor  
10 pixels to reduce image lag.